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2819

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March 2, 2004

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Edward A. Ramsden
Assignee: Lattice Semiconductor Corporation
Title: Programmable Logic Device With Hardwired Microsequencer
Serial No.: 10/617,980 Filed: 07/10/2003
Examiner: Unknown Group Art Unit: 2819
Docket No.: M-15145 US

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (in duplicate); and
- (3) Change of Correspondence Address.



No additional fee is required.



The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After</u> <u>Amendment</u>		Highest No. Previously <u>Paid</u> <u>For</u>		Present <u>Extra</u>		<u>Rate</u>		<u>Additional Fee</u>
Total Claims	20	Minus	20	=	0	x	\$18.00	\$	0
Independent Claims	3	Minus	3	=	0	x	\$86.00	\$	0
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application								\$
<input type="checkbox"/>	Fee for Request for Extension of Time								\$

Total additional fee for this Amendment:

\$

- ☒ Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.
- ☐ Please charge our Deposit Account No. 50-2257 in the amount of \$
- ☒ Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 50-2257

Total: \$

0

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 2, 2004.

Eric Hoover

March 2, 2004
Date of Signature

Respectfully submitted,

Jon W. Hallman
Attorney for Applicants
Reg. No. 42,622



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Edward A. Ramsden
Assignee: Lattice Semiconductor Corporation
Title: Programmable Logic Device With Hardwired Microsequencer
Serial No.: 10/617,980 Filing Date: 07/10/2003
Examiner: Unassigned Group Art Unit: 2819
Docket No.: M-15145 US

Irvine, California
March 2, 2004

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

CHANGE OF CORRESPONDENCE ADDRESS

Dear Sir:

Pursuant to 37 C.F.R. § 1.33(a)(2), the undersigned principal attorney of record hereby requests that the correspondence address for the above-referenced patent application be changed to:

MacPherson Kwok Chen & Heid LLP
1762 Technology Drive, Suite 226
San Jose, CA 95110

The day time telephone number of the undersigned Attorney for Applicant is (949) 752-7040. The Examiner is requested to telephone the undersigned Attorney for Applicant, if the Examiner has any question regarding the above.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on March 2, 2004.

Eric Hoover

3/2/2004

Date of Signature

Respectfully submitted,

Jon W. Hallman
Attorney for Applicant(s)
Reg. No. 42,622

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